

Fig. 1

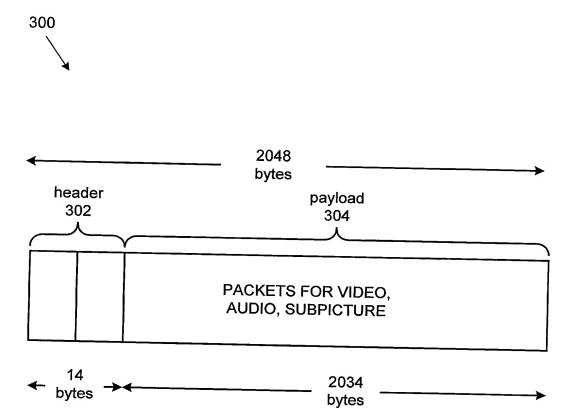
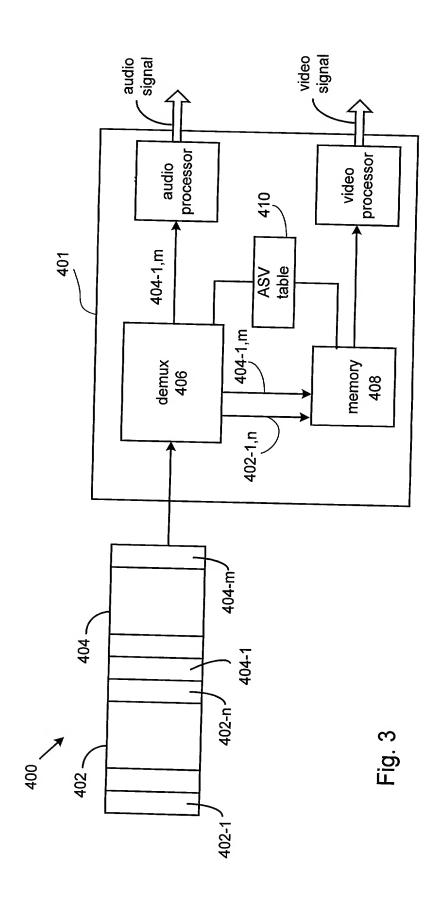


Fig. 2



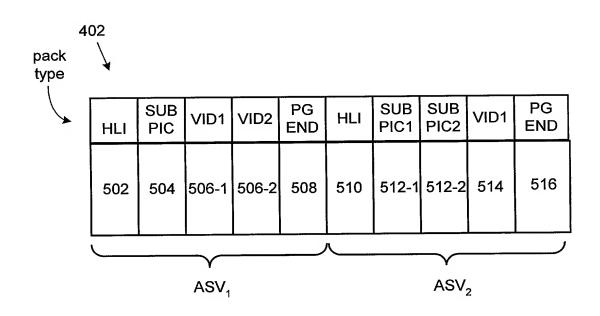


Fig. 4

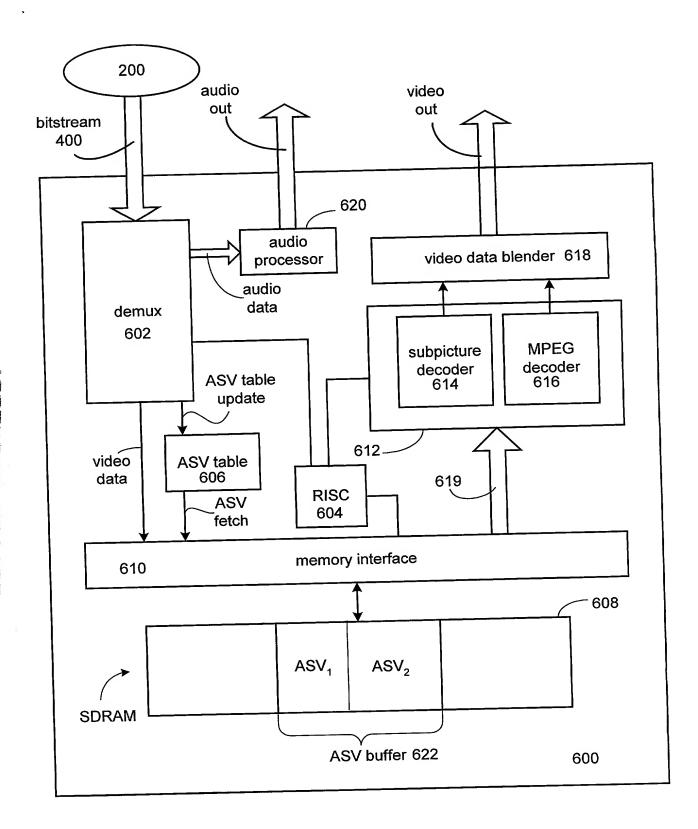


Fig. 5

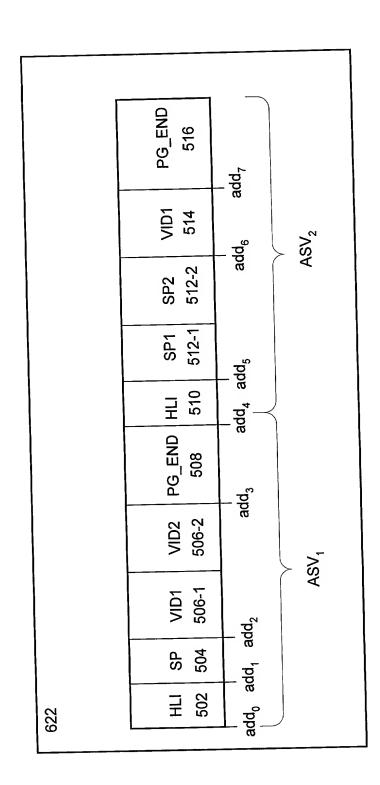


Fig. 6

	HLI	SP	VIDEO	RESERVED	
ASV ₀	start add ₁	start add ₂	start add ₃		
ASV1	start add₄	start add ₅	start add ₆		
ASV ₂	start add ₇	start add ₈	start add ₉		
	<u> </u>				
	 				
ASV _n	start add _{n-2}	start add _{n-1}	start add _n		

800

Fig. 7



	HLI	SP	VIDEO	
ASV ₁	add ₀ [1,1]	add ₁ [1,2]	add ₂	
ASV ₂	add₄	add ₅	add ₆	

Fig. 8

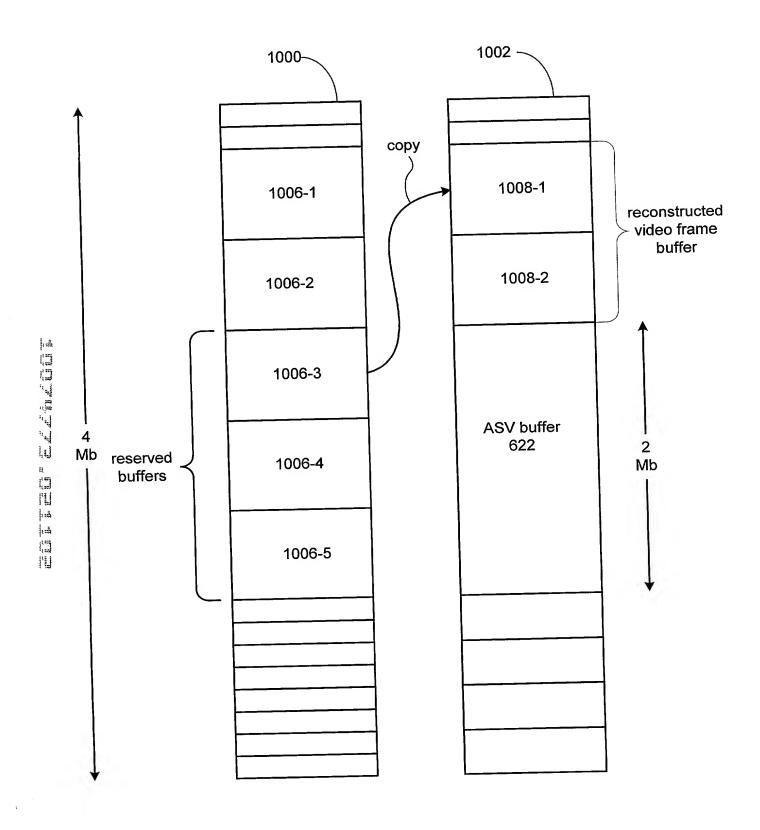


Fig. 9

